

PATENT

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Date July 15, 2008


Sherri A. Phillips

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/822,275

Confirmation No. : 2177

Applicant : Jeffery W. Janzen

Filed : April 8, 2004

Attorney Docket No.: 501286.01 (30262/US)

Art Unit : 2188

Customer No. : 27,076

Examiner : Craig E. Walter

Title : SYSTEM AND METHOD FOR OPTIMIZING INTERCONNECTIONS OF
COMPONENTS IN A MULTICHIP MEMORY MODULE

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

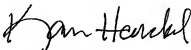
Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicant wishes to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449. Copies of the cited U.S. patents and U.S. patent application publications will not be submitted herewith in accordance with the waiver by the Office of the requirement under 37 C.F.R. § 1.98(a)(2)(i) for U.S. national patent applications filed after June 30, 2003. A copy of the cited non-patent literature reference, as required under 37 C.F.R. § 1.98(a)(2), is enclosed. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicant's duty to disclose all information he is aware of which is believed relevant to the examination of the above-identified application, applicant believes that his invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

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Enclosures:

Form PTO-1449

Cited Reference (1)

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